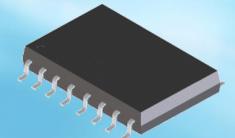


# Smart Gate Driver Coupler TLP5214 Application Note -Introduction-



© 2014, Toshiba Corporation.

Rev.1.0 Aug/12/2014

## Introduction

TLP5214 is a general purpose gate driver coupler to which we have added a V<sub>CE(sat)</sub> detection function, miller clamp function, FAULT detection function, and more, for a driver coupler equipped with functions to protect the IGBT from overcurrent (such as generated in inverter circuits). These photocouplers use the 16-pin SO16L package to handle multifunction expansion. This document describes these newly added functions.

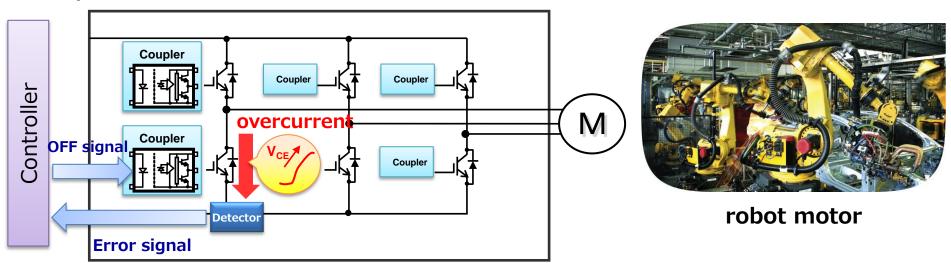
	General purpose gate driver coupler	Smart gate driver coupler	
Model	TLP352, TLP5701, etc.	TLP5214	
Package/Internal circuit diagram	DIP8, SO6L etc.	SO16L	
Pin count	8 pin or 6 pin	16 pin	
IGBT gate direct drive			
UVLO function			
V <sub>CE(sat)</sub> detection function	-		
Active miller clamp	-		
FAULT feedback	-		

TOSHIBA Leading Innovation >>>

## What is overcurrent protection?

Overcurrent protection detects abnormal currents in a circuit and prevents them from damaging the circuit. For overcurrents in IGBTs used in inverter circuits, stopping any overcurrent as fast as possible is necessary since there is a risk of the powerful load damaging the circuit when the voltage ( $V_{CE}$ ) between collector and emitter rises. The time from the start of an overcurrent until the IGBT is damaged is called the short circuit capability. It is necessary to stop the overcurrent within the short circuit capability time period. The short circuit capability depends on the product or junction temperature, etc., but circuits must be designed such that the overcurrent is cut off within 10 µs. Also, many electrical products are required by safety regulations to have features that protect humans from overcurrent conditions.

#### **Example inverter circuit**



## **Causes of overcurrent in inverter circuits**

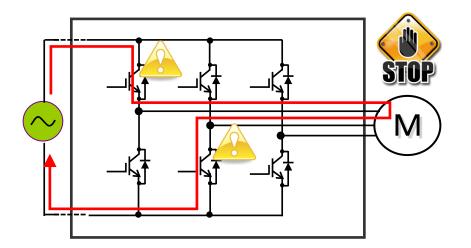
There are various reasons for overcurrent conditions - some typical examples are given here. When the overcurrent protector is activated, the inverter stops operating and can be restarted when the cause is removed.

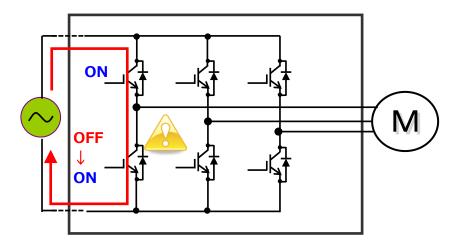
#### **Output short circuit:**

An overcurrent flows between two opposite arms due to a human error, causing a misconnection or load that causes damage. Or, an overcurrent condition may be caused by the motor being mechanically impeded by some external factor.

### Arm short circuit:

An overcurrent condition occurs when the upper and lower arms short circuit. Two examples are ... (1) malfunction caused by the miller current during switching (2) malfunction due to noise.





## **Types of overcurrent protection**

There are several ways to prevent IGBT destruction caused by overcurrent. Each of them has different advantages. Number 3, monitoring the saturation voltage VCE(sat) of the IGBT, has the advantage of (1) low power loss, and, (2) the protection function can be done on the IGBT side, so it works quickly. The TLP5214 photocoupler monitors VCE(sat) while driving an IGBT in a protection circuit.

### 1. Current transformer(CT)

Current transformer for high frequency AC or DC currents are used to monitor currents. Electrical isolation is not necessary, but it may be physically too large for the latest compact inverters.

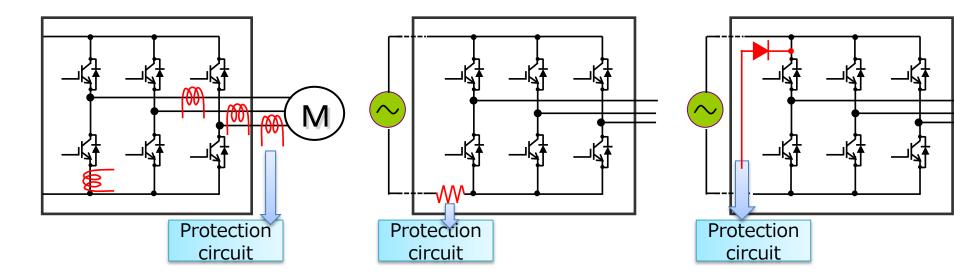
#### 2. Current sense resistor

A metallic resistor for sensing the current is used to monitor the current (insulation is needed). Although relatively small, it produces large power losses due to the load current.

## 3. V<sub>CE(sat)</sub> monitor

A high voltage diode is used to monitor the collector-emitter voltage. This circuit has several advantages, including ...

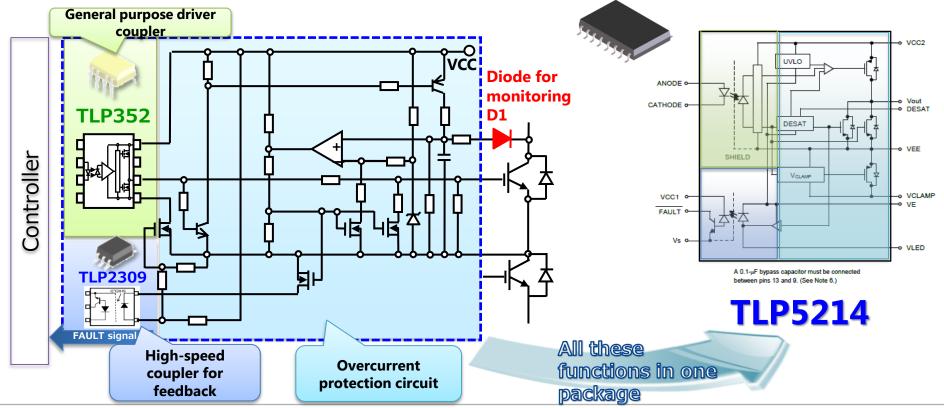
- 1. low power loss
- 2. relatively inexpensive
- 3. fast operation



## Example of the TLP352 with a protection circuit, and the TLP5214

The diagram on the lower left shows an example of a protection circuit that uses the No.3 VCE(sat) monitor (from the previous page) in the TLP352 general-purpose driver coupler. In the protection circuit, the VCE(sat) voltage is monitored as it goes through diode D1, so the gate signal of the IGBT turns off softly when an overcurrent is detected. Also, a 1 Mbps-class high-speed coupler is used to transmit fault signals to the controller. The external components of the control circuit are complicated and use much of the PWB space, but the TLP5214 photocoupler provides a smart gate drive circuit and overcurrent protection with all these functions in a single package.

#### **Example inverter circuit protection:**



TOSHIBA Leading Innovation >>>

© 2014, Toshiba Corporation.

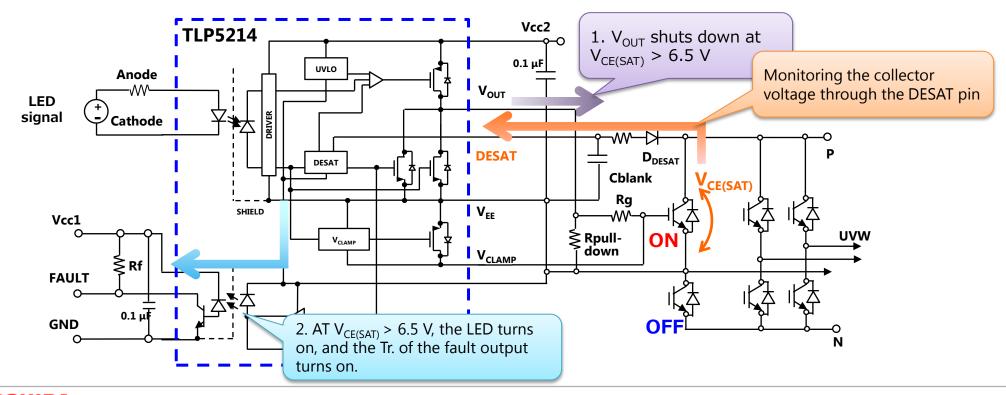
## **Overview of protection functions**

With the TLP5214, the DESAT pin is used to monitor the voltage ( $V_{CE}$ ) between the collector and the emitter of the IGBT through the external diode  $D_{DESAT}$ . Normally, when the IGBT is in an ON state,  $V_{CE}$  becomes the saturation voltage  $V_{CE(sat)}$  (under about 2 V), but when an overcurrent occurs it enters a non-saturation state and  $V_{CE(sat)}$  rises. The TLP5214 performs two operations when the  $V_{CE(sat)}$  exceeds 6.5 V (standard).

## **1.** $V_{OUT}$ softly turns off and prevents the overcurrent from damaging the IGBT.

#### 2. A fault signal is transmitted to the controller.

Normally, the fault signal is fed back to the controller, and a few  $\mu$ s pass before the LED signal/coupler output stops. The TLP5214, however, provides excellent circuit protection that is fast and reliable because the V<sub>OUT</sub> shut down starts in less than 700 ns.



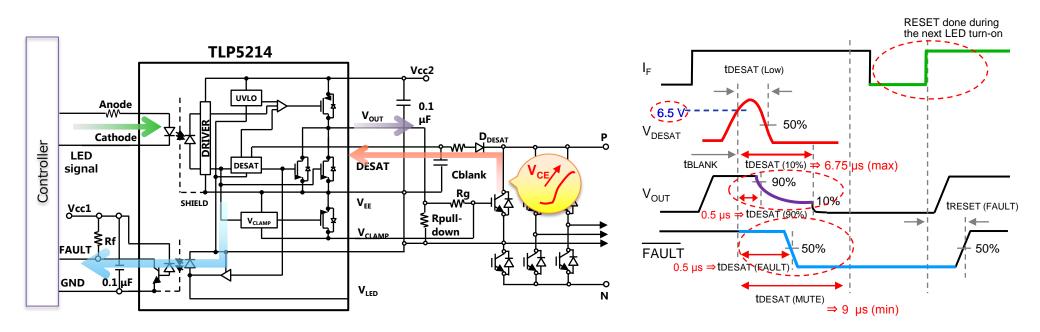
TOSHIBA Leading Innovation >>>

## **Protection operations and reset method**

With the TLP5214, when the protection circuit operates, LED signals are not accepted until a set period of time elapses,  $t_{DESAT(MUTE)}$ . The reset of the protection operation is initiated by the LED signal after the  $t_{DESAT(MUTE)}$  time period has elapsed. The flow of operations from start to reset of the protection operation is shown below.

- **1**. When an overcurrent occurs the V<sub>CE</sub> of the IGBT rises, when it exceeds 6.5 V the protection operation starts
- 2. Coupler output does a soft shutdown (to protect the IGBT from secondary damage caused by wiring inductance)
- 3. The fault pin falls to the L level because the signal is fed back to the controller

4. The next LED signal following the occurrence of the protection operation resets the protection operation



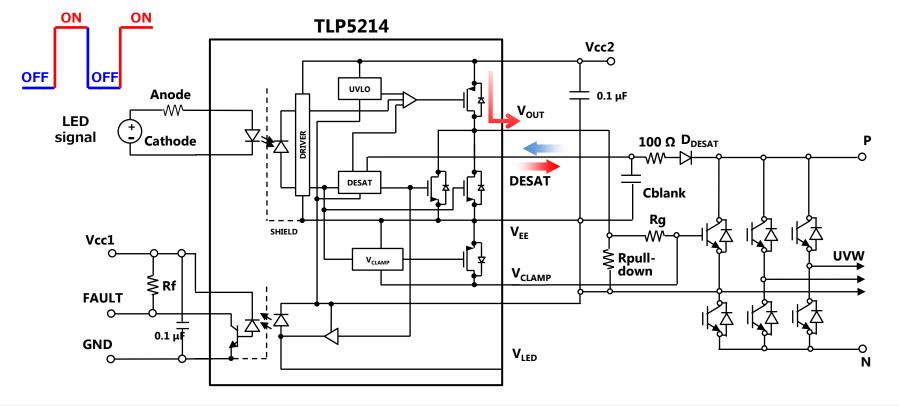
 $\ensuremath{\textcircled{\sc c}}$  2014, Toshiba Corporation.

## **Normal operation**

**LED signal OFF**:  $V_{OUT}$  is low level when the LED signal is off and the DESAT pin is non-active. A fault signal is not sent even if more than the  $V_{DESAT}$  voltage is applied to the DESAT pin.

**LED signal ON**:  $V_{OUT}$  is high level when the LED signal is on and the DESAT pin is active. The above protection operation is triggered when an overcurrent condition occurs.

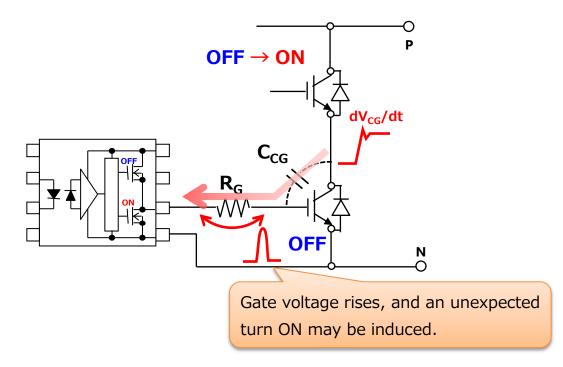
LED	DESAT input	FAULT output	V <sub>OUT</sub>
OFF	Non-active	High	Low
ON	Active Low ( < V <sub>DESATth</sub> )	High	High
ON	Active High ( > V <sub>DESATth</sub> )	Low (FAULT)	Low



## Malfunction due to miller capacitance

Malfunctions include parasitic miller capacitance,  $C_{CG}$ , between the collector gates of the IGBT, caused by switching noise in the inverter. This malfunction mechanism and the miller clamp function on the TLP5214 are explained here.

When the IGBT of the upper arm in the inverter circuit turns on, the V<sub>CE</sub> of the IGBT of the lower arm rises sharply.\* At this time, the C<sub>CG</sub> of the lower IGBT is introduced and the displacement current I<sub>S</sub> =  $C_{CG} \times (dV_{CG}/dt)$  is generated and flows in the direction of the photocoupler's output. As the current passes through the gate resistor R<sub>G</sub> of the circuit, the voltage drops and the gate voltage rises, generating a false ON condition of the IGBT, which induces a short circuit in the upper/lower arms.



\*Depending on the circuit, dV<sub>CG</sub>/dt may be generated during the dead time when the lower arm IGBT goes from on to off.

© 2014, Toshiba Corporation.

Leading Innovation >>>

## Preventing a malfunction due to miller capacitance

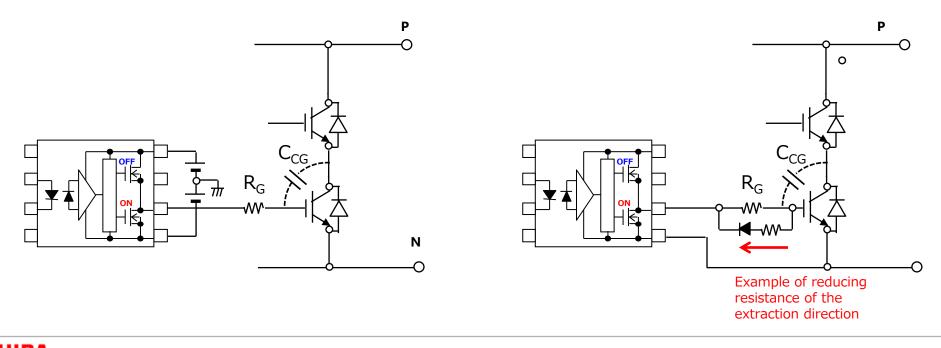
Below are two measures to prevent a malfunction due to miller capacitance. The first involves using a negative power source. The 2<sup>nd</sup> involves adjusting the gate resistance. Using a negative supply voltage requires a power circuit, so cost and size can be an issue. The switching noise induced by adjusting the gate resistance also has an effect. In addition, there is a limit to lowering the gate resistance.

#### 1. Using a negative supply voltage

Using a negative supply voltage for the photocoupler's power source prevents this malfunction because the gate has a negative voltage when the IGBT is off. Since this solution requires a negative power supply circuit, cost and size can be an issue.

### 2. Adjusting the gate resistance

The increasing in gate potential can be minimized by using a smaller gate resistance. Although this solution is less inexpensive than using a negative power supply, switching noise may increase.

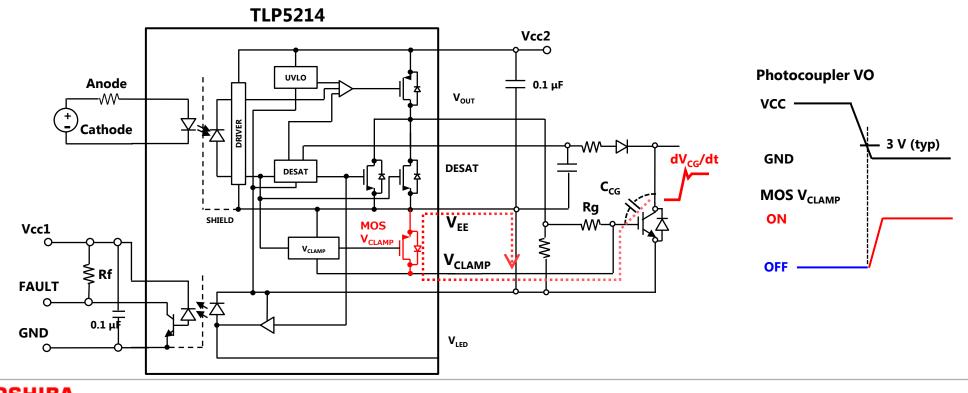


## **Active miller clamp function**

Another method to prevent a malfunction caused by miller capacitance includes making a short circuit between the gate emitters of the IGBT. Configuring a circuit to clamp the gate safely from an external component is complicated and requires additional PWB space (refer to page 5 and 6). The TLP5214 has an internal function, called the active miller clamp function, that connects the gate emitters of the IGBT – this eliminates the need for an external component in the clamp circuit.

The miller clamp pin  $V_{CLAMP}$  is connected to the gate of the IGBT.

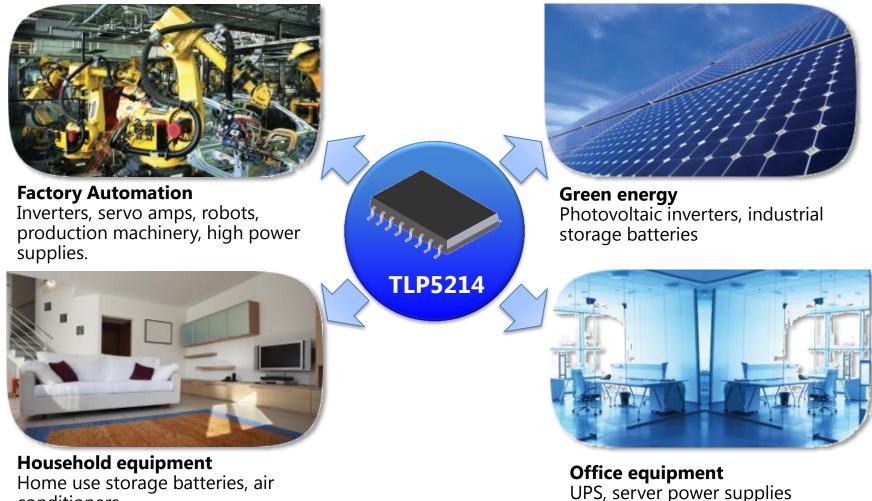
The output of the photocoupler switches from high to low, the gate voltage goes under about 3 V, so the MOSFET between  $V_{CLAMP}-V_{EE}$  turns on and the gate clamps on the emitter ( $V_{EE}$ ). This causes the miller current to bypass to the emitter from the  $V_{CLAMP}$  pin, which suppresses the rise in voltage at the gate, and prevents short circuits on the upper/lower arms.



TOSHIBA Leading Innovation >>>

# **Application**

Inverter circuits are used in a wide range of applications, ranging mainly from general purpose inverters, power conditioners for solar power generation and other industrial equipment, to UPS, home use storage cells and other household equipment.



Leading Innovation >>>

© 2014, Toshiba Corporation.

Rev.1.0 Aug/12/2014

# **RESTRICTIONS ON PRODUCT USE**

- Toshiba Corporation, and its subsidiaries and affiliates (collectively "TOSHIBA"), reserve the right to make changes to the information in this document, and related hardware, software and systems (collectively "Product") without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which Minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before customers use the Product, create designs including the Product, or incorporate the Product into their own applications, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application with which the Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) deterMining the appropriateness of the use of this Product in such design or applications; (b) evaluating and deterMining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications.
- PRODUCT IS NEITHER INTENDED NOR WARRANTED FOR USE IN EQUIPMENTS OR SYSTEMS THAT REQUIRE EXTRAORDINARILY HIGH LEVELS OF QUALITY AND/OR RELIABILITY, AND/OR A MALFUNCTION OR FAILURE OF WHICH MAY CAUSE LOSS OF HUMAN LIFE, BODILY INJURY, SERIOUS PROPERTY DAMAGE AND/OR SERIOUS PUBLIC IMPACT ("UNINTENDED USE"). Except for specific applications as expressly stated in this document, Unintended Use includes, without limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, medical equipment, equipment used for automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or explosions, safety devices, elevators and escalators, devices related to electric power, and equipment used in finance-related fields. IF YOU USE PRODUCT FOR UNINTENDED USE, TOSHIBA ASSUMES NO LIABILITY FOR PRODUCT. For details, please contact your TOSHIBA sales representative.
- Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE FOR PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND LOSS OF DATA, AND (2) DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO SALE, USE OF PRODUCT, OR INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.
- GaAs (Gallium Arsenide) is used in Product. GaAs is harmful to humans if consumed or absorbed, whether in the form of dust or vapor. Handle with care and do not break, cut, crush, grind, dissolve chemically or otherwise expose GaAs in Product.
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile technology products (mass destruction weapons). Product and related software and technology may be controlled under the applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the U.S. Export AdMinistration Regulations. Export and re-export of Product or related software or technology are strictly prohibited except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. Please use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. TOSHIBA ASSUMES NO LIABILITY FOR DAMAGES OR LOSSES OCCURRING AS A RESULT OF NONCOMPLIANCE WITH APPLICABLE LAWS AND REGULATIONS.

# **Revision History**

Revision	Date	Page	Description
Rev.1.0	Aug/12/2014	-	First edition